

MSC81325M

PRELIMINARY DATA

RF & MICROWAVE TRANSISTORS AVIONICS APPLICATIONS

- REFRACTORY/GOLD METALLIZATION
- EMITTER BALLASTED
- RUGGEDIZED VSWR ∞:1
- INPUT/OUTPUT MATCHING
- OVERLAY GEOMETRY
- METAL/CERAMIC HERMETIC PACKAGE
- P_{OUT} = 325 W MIN. WITH 6.7 dB GAIN

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.400 x .400 2NLFL (S042) hermetically sealed

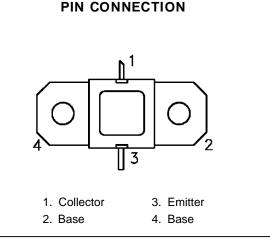
ORDER CODE MSC81325M BRANDING 81325M

DESCRIPTION

The MSC81325M device is a high power pulsed transistor specifically designed for DME/TACAN avionics applications.

This device is capable of withstanding an infinite load VSWR at any phase angle under full rated conditions. Low RF thermal resistance and semiautomatic bonding techniques ensure high reliability and product consistency.

The MSC81325M is housed in the industry-standard AMPACTM metal/ceramic hermetic package with internal input/output matching structures.



ABSOLUTE	MAXIMUM	RATINGS	$(T_{case} = 25^{\circ})$	°C)
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Symbol	Parameter	Value	Unit	
Pdiss	Power Dissipation [*] $(T_C \le 100^{\circ}C)$	880	W	
Ι _C	Device Current*	24	А	
V _{CC}	Collector-Supply Voltage*	55	V	
TJ	Junction Temperature (Pulsed RF Operation)	250	°C	
T _{STG}	Storage Temperature	– 65 to +200	°C	

THERMAL DATA

RTH(j-c)	Junction-Case Thermal Resistance*	0.17	°C/W	
*Applies only to rated RF amplifier operation				

MSC81325M

ELECTRICAL SPECIFICATIONS $(T_{case} = 25^{\circ}C)$

STATIC

Symbol Test Conditions	Test Conditions	Value			11		
		Min.	Тур.	Max.	Unit		
BV _{CBO}	$I_{C} = 10 \text{mA}$	$I_E = 0mA$		65	_		V
BVEBO	$I_E = 1mA$	$I_C = 0 m A$		3.5	—		V
BVCER	IC = 25mA	$R_{BE} = 10\Omega$		65	_	_	V
ICES	$V_{BE} = 0V$	$V_{CE} = 50V$		_	_	25	mA
h _{FE}	$V_{CE} = 5V$	$I_C = 1A$		15		120	_

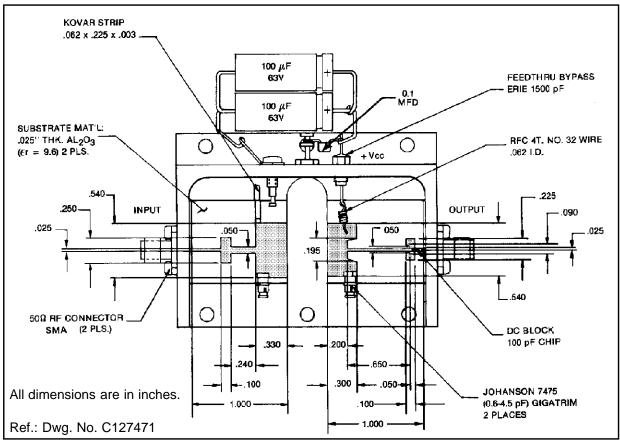
DYNAMIC

Symbol	Toot Conditions		Value			11:4
Symbol Test Conditions		Min.	Тур.	Max.	Unit	
Роит	$f = 1025 - 1150 \text{ MHz} P_{IN} = 70 \text{ W} V_C$	c = 50 V	325	360		W
ης	$f = 1025 - 1150 \text{ MHz} P_{IN} = 70 \text{ W} V_C$	c = 50 V	40	41		%
GP	$f = 1025 - 1150 \text{ MHz} P_{IN} = 70 \text{ W} V_C$	c = 50 V	6.7	7.1	_	dB

Note: Pulse Width = 10μ Sec

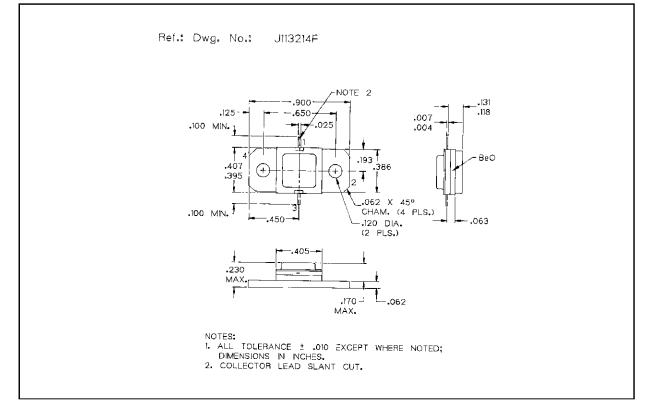
Duty Cycle = 1%

TEST CIRCUIT





PACKAGE MECHANICAL DATA



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